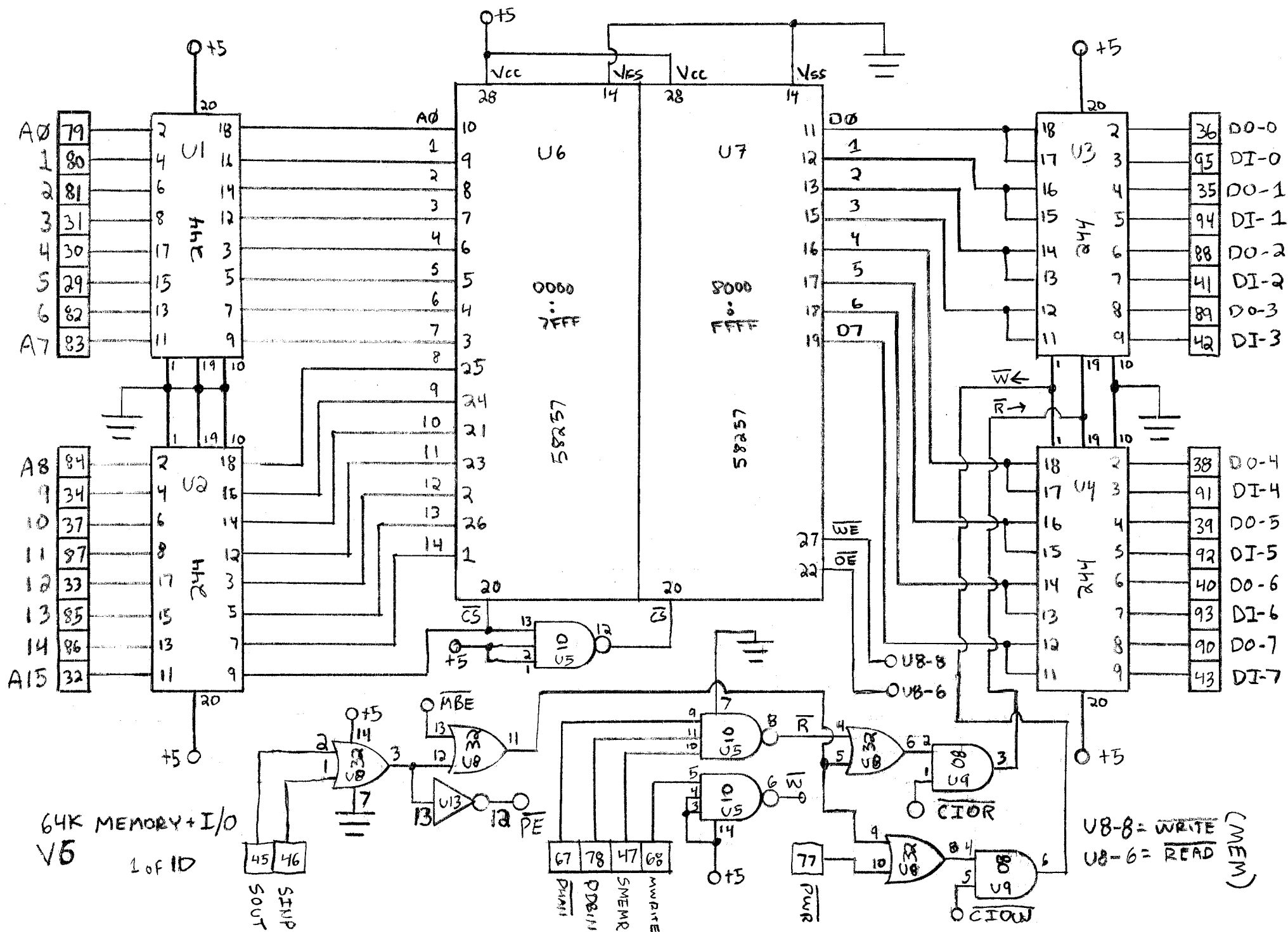
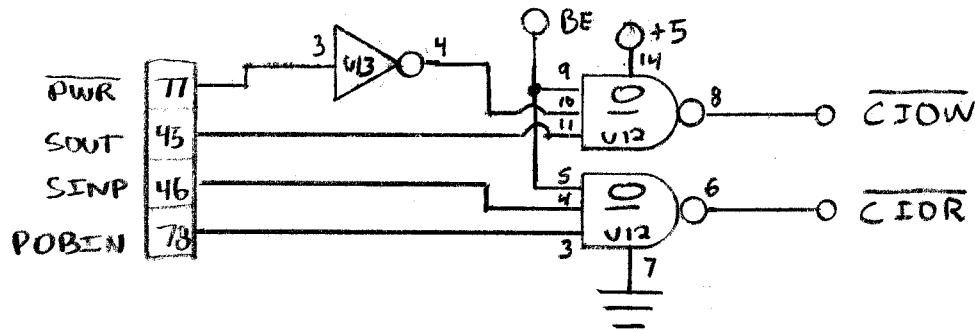
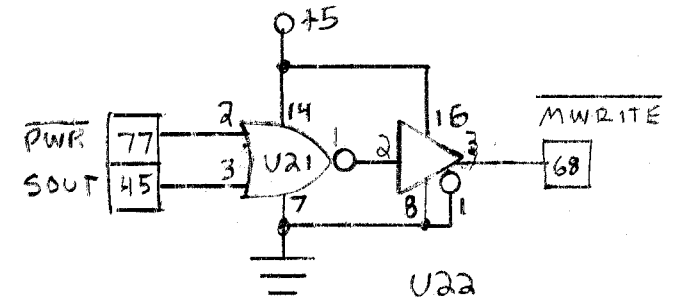
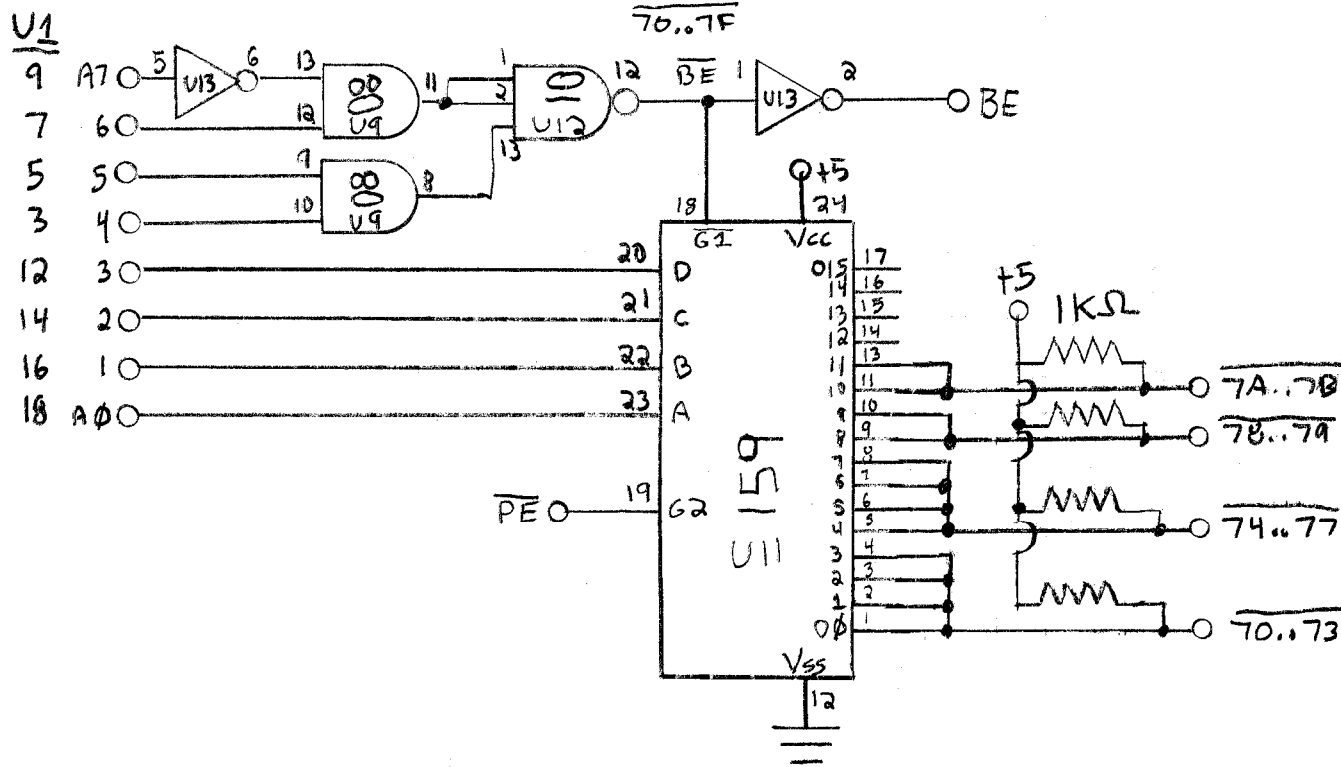
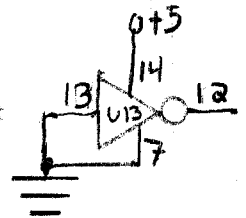


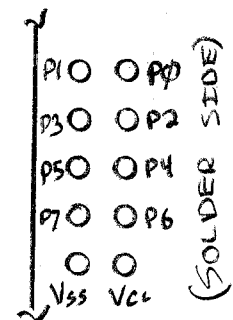
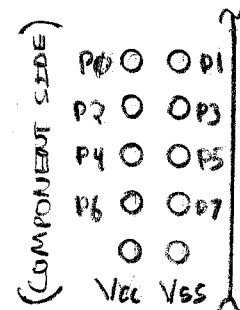
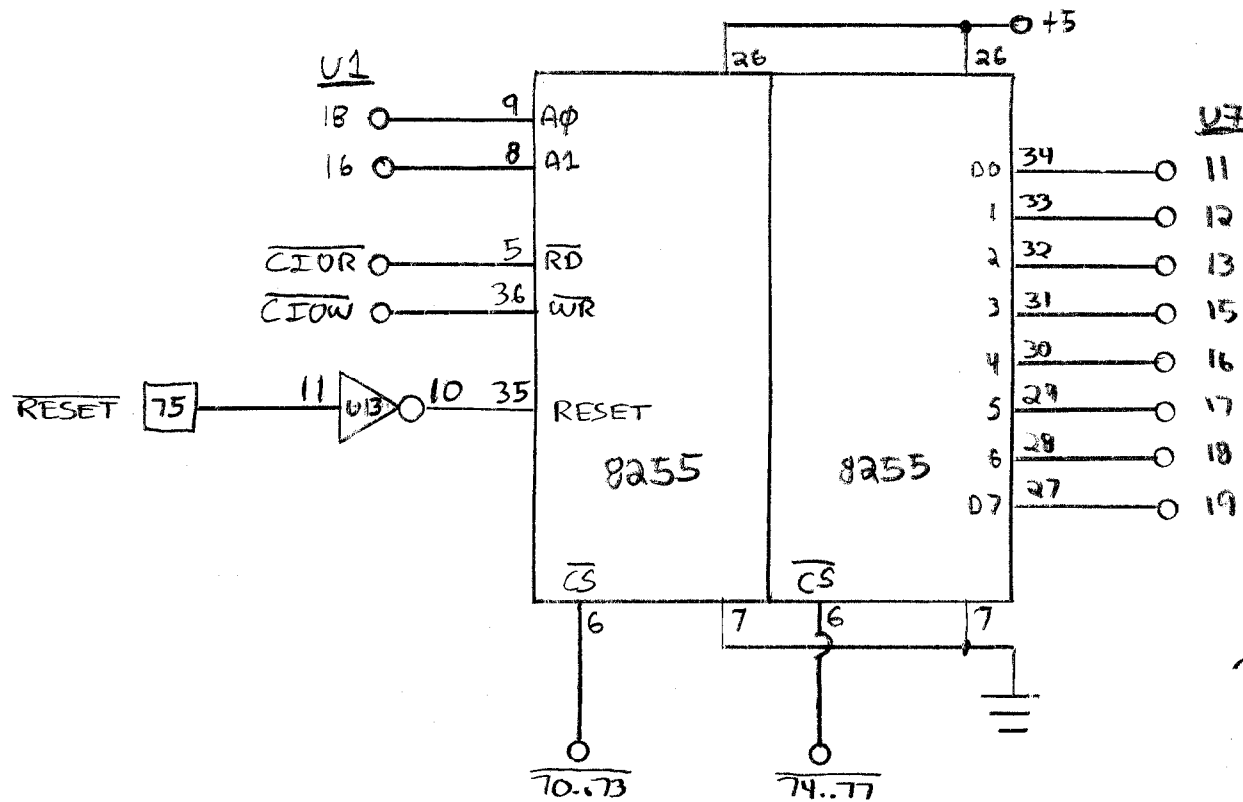




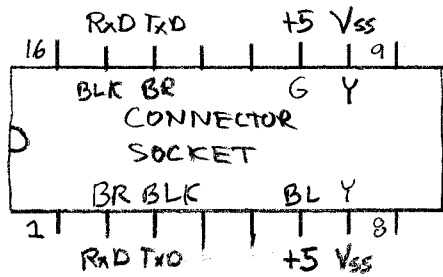
$\overline{PE}$ = Port I/O ENABLE 00..FF
 $\overline{BE}$ = Block Enable 70..7F
 $\overline{CIOR}$ = I/O Read on 70..7F
 $\overline{CIOW}$ = I/O Write on 70..7F



64K Memory + I/O
 V6 2.0F 10

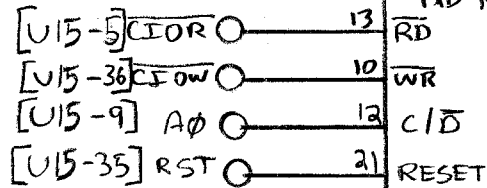


64K Memory + IO
 V5
 30F 10



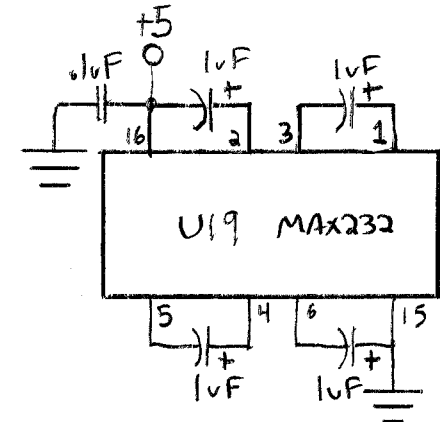
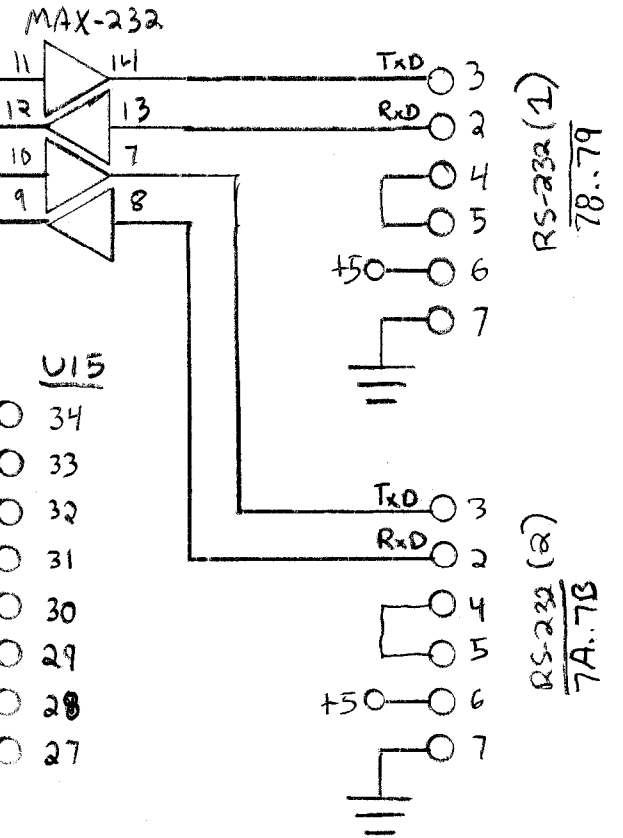
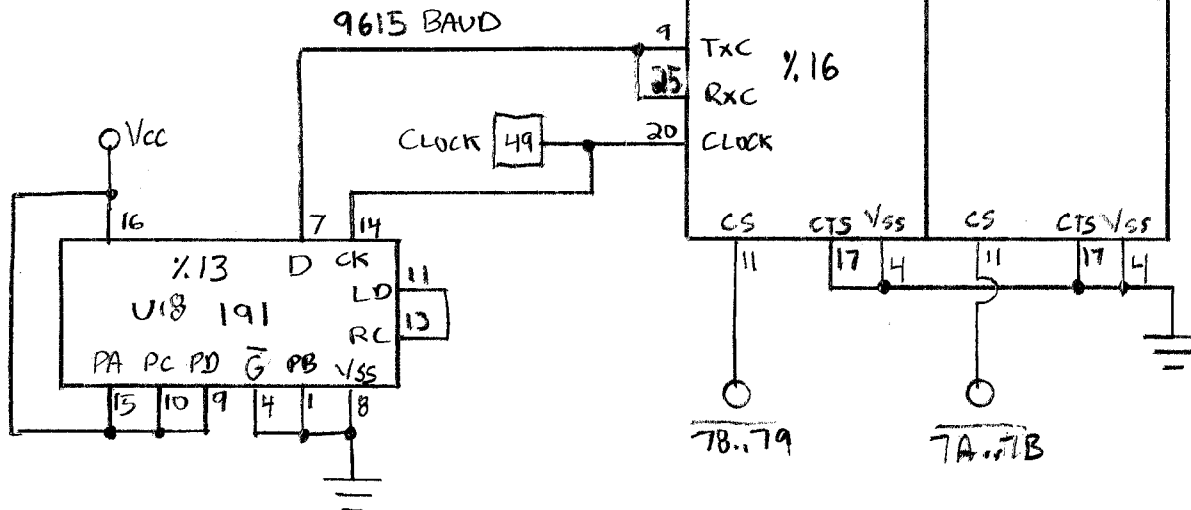
7A..7B

78..79

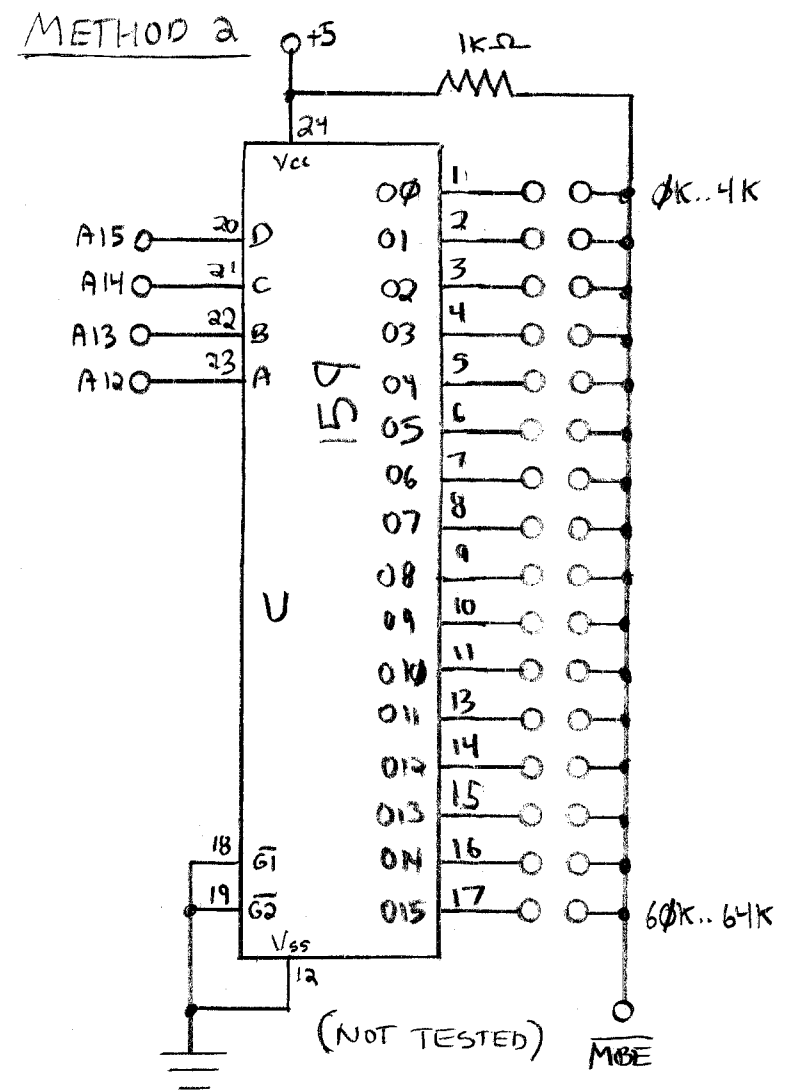
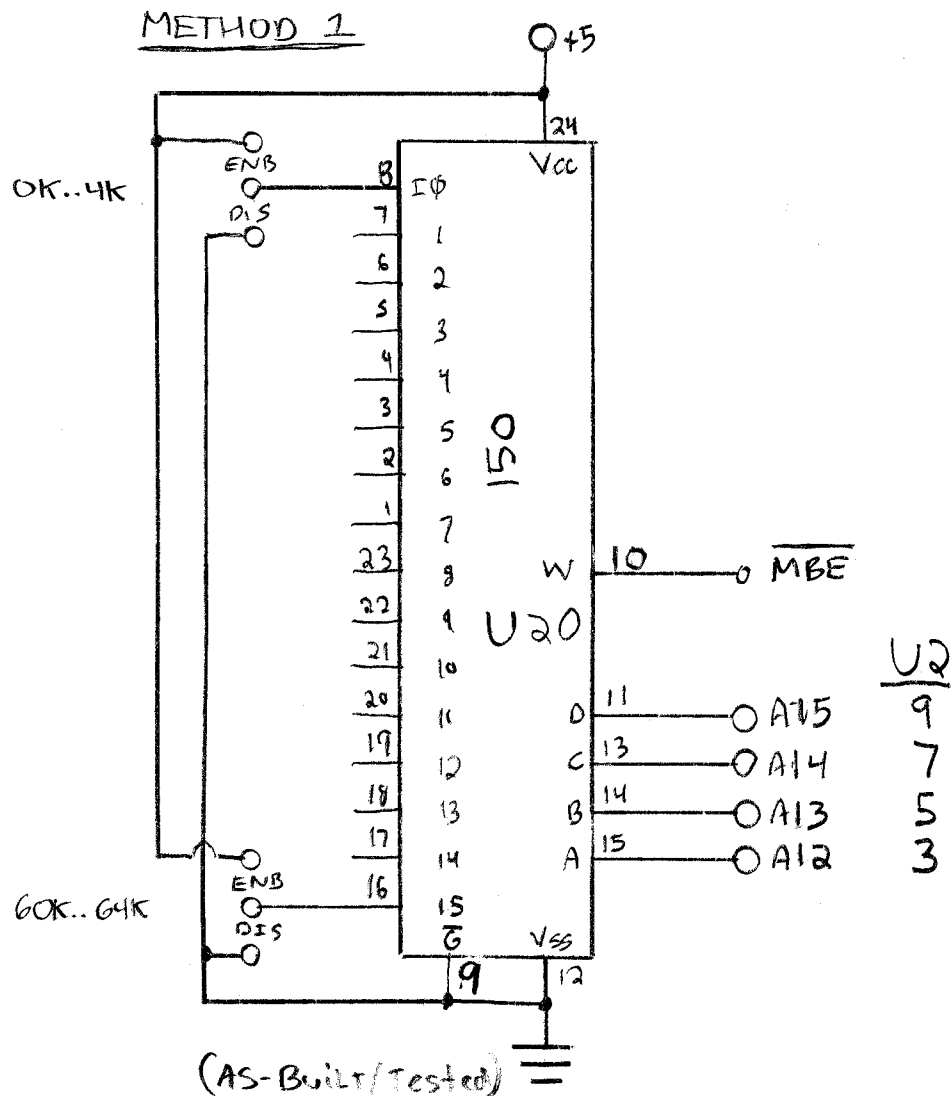


	I/O CARD	(OR206)
78..79	CCOM 79H 121	03H
	CDATA 78H 120	02H
7A..7B	CCOM 7BH 123	
	CDATA 7AH 122	

9615 = 2,000,000 %13 %16 (U18) (U16/U17)



64K Memory + I/O
V6 40F 10



<u>70..73</u>	READ or WRITE ON Port 70 to 73 - 8255 #1
<u>74..77</u>	" " " " " 74 to 77 - 8255 #2
<u>78..79</u>	" " " " " 78 or 79 - 8250 #1
<u>7A..7B</u>	" " " " " 7A or 7B - 8250 #2

BE BLOCK ENABLE - A7..A0 = 0111XXXXB 7Xh
BE BLOCK ENABLE = " " " " " "

PE PORT ENABLE = PORT BEING ACCESSED - SOUT or SINP is High

CIOR COMPUTER I/O READ = PORT READ IS REQUESTED
CIDW COMPUTER I/O WRITE = PORT WRITE IS REQUESTED

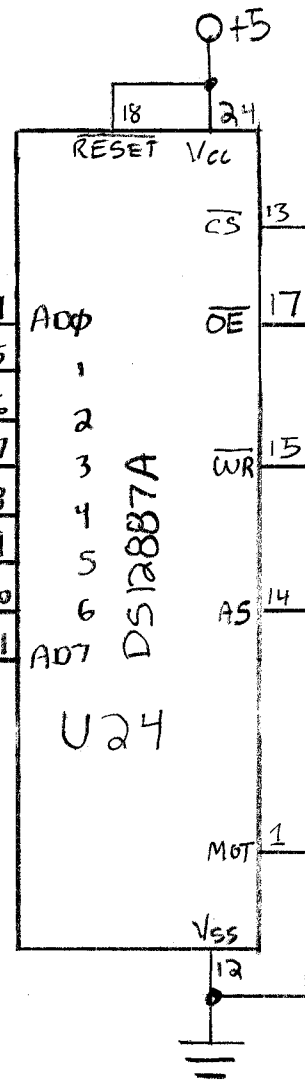
R READ = MEMORY READ IS REQUESTED
W WRITE = MEMORY WRITE IS REQUESTED

MBE MEMORY BLOCK ENABLE = LOW MEANS 4K Block (A15..A12) is enabled.

$\overline{WR}$ 7C = Address
 $\overline{RD}$ 7D = Data
 $\overline{WR}$ 7D = Data

U17

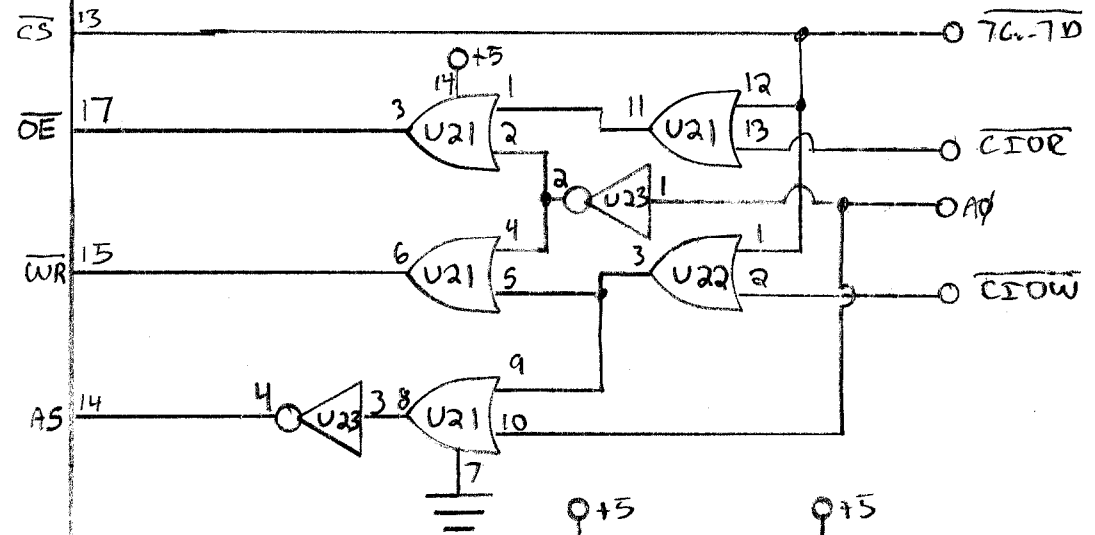
27 D0
 28 10
 1 20
 2 30
 5 40
 6 50
 7 60
 8 D70



DS12887A

U24

Generate AS Pulse

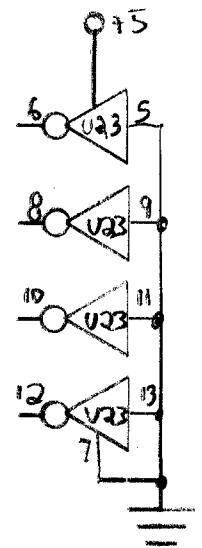
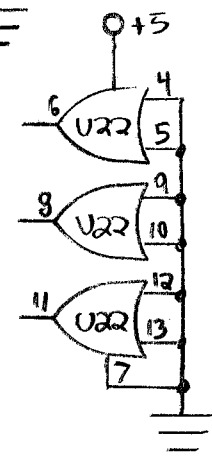


7C-7D

$\overline{CSOE}$

AD

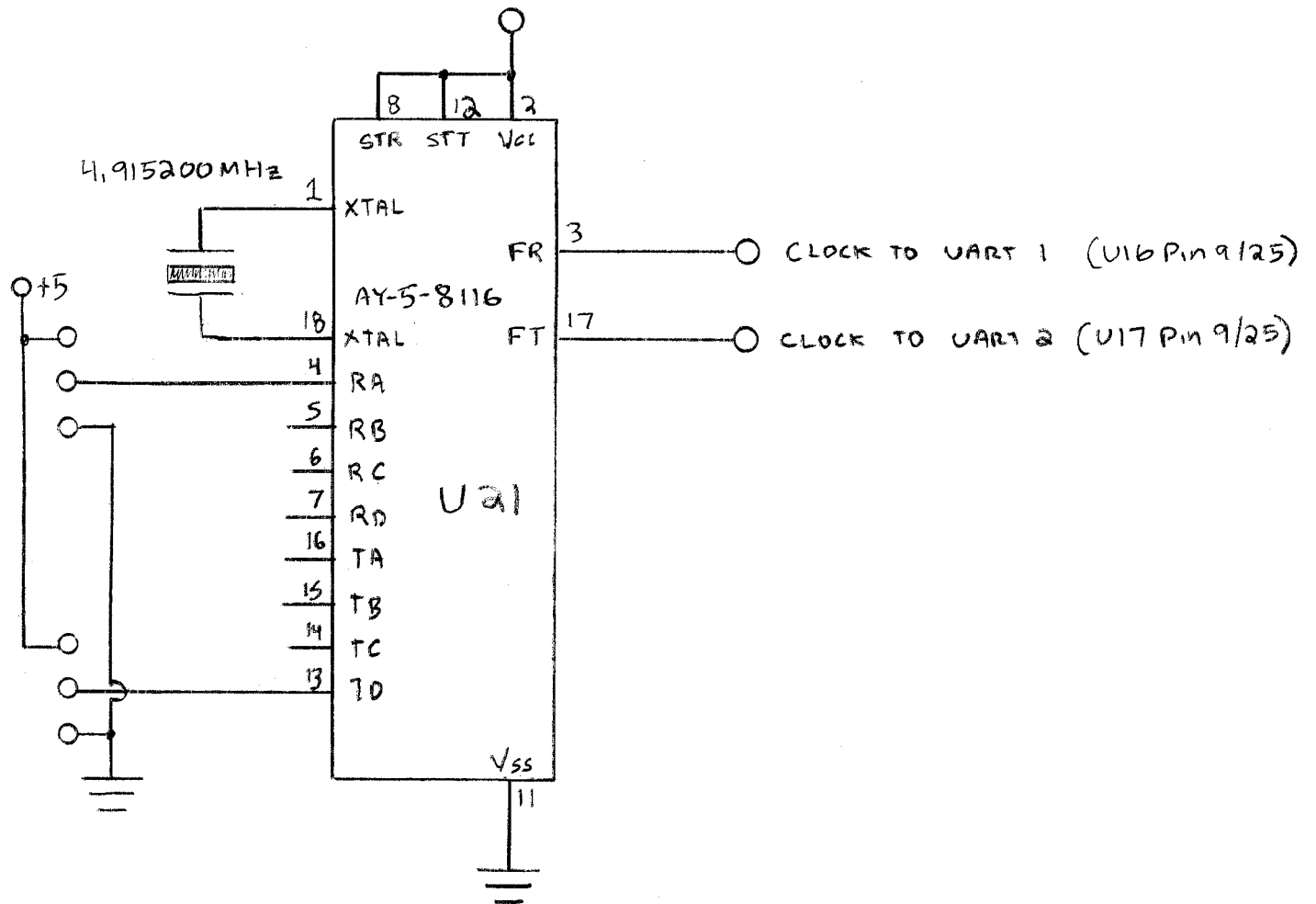
$\overline{CSOW}$



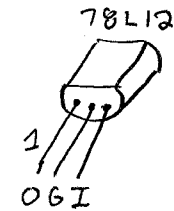
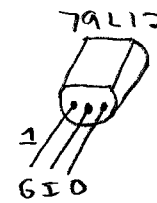
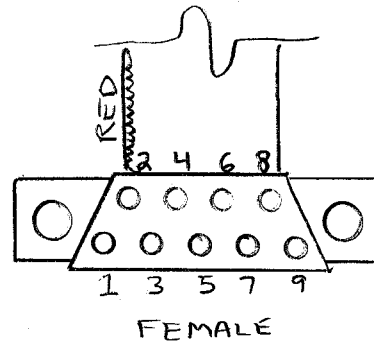
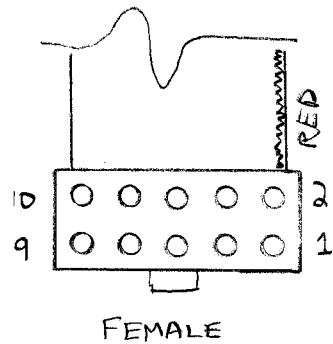
64K Memory, + I/O (RTC Module) (NOT TESTED)
 V6 7 of 10

64K MEMORY + I/O
V6 8 OF 10

TD	TC	TB	TA	BAUD
RD	RC	RB	RA	
0	0	0	0	50
0	0	0	1	75
0	0	1	0	110
0	0	1	1	134
0	1	0	0	150
0	1	0	1	300
0	1	1	0	600
0	1	1	1	1200
1	0	0	0	1800
1	0	0	1	2000
1	0	1	0	2400
1	0	1	1	3600
1	1	0	0	4800
1	1	0	1	7200
1	1	1	0	9600
1	1	1	1	19200

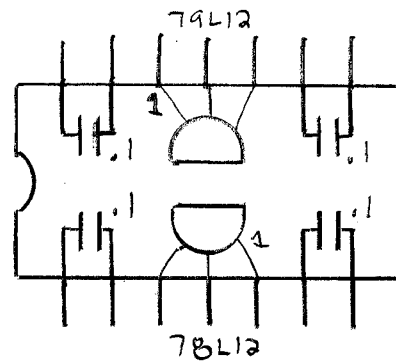


BAUD RATE GENERATOR
9 of 10

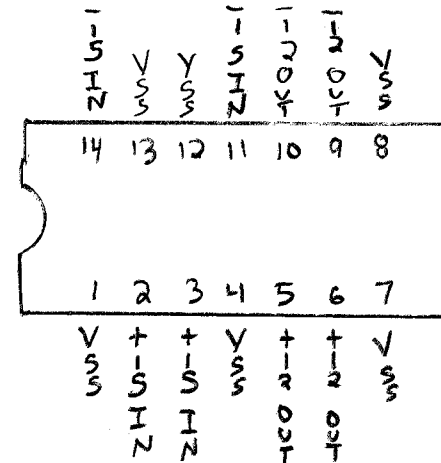


G = GND/VSS
I = V-IN
O = V-OUT

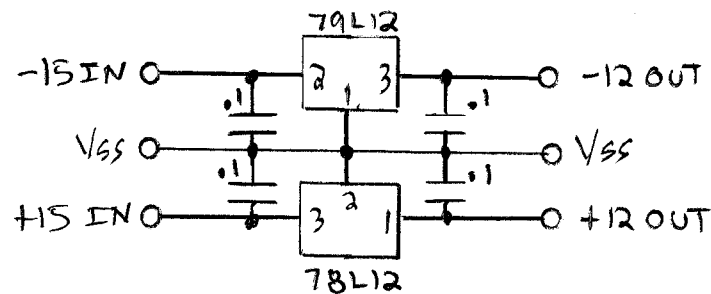
PIN	FUNCTION
1-2	Vcc +5
3-4	VSS
5	+12VDC
6	-12VDC
7	RESET
8	RESET
9	TxD
(10)	UNUSED



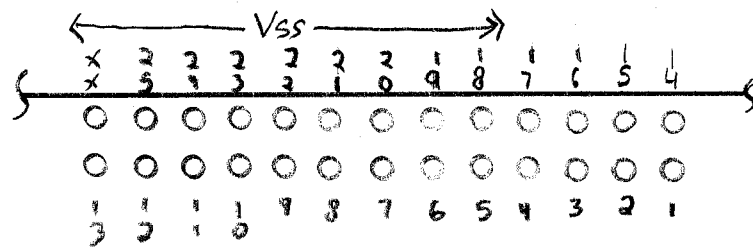
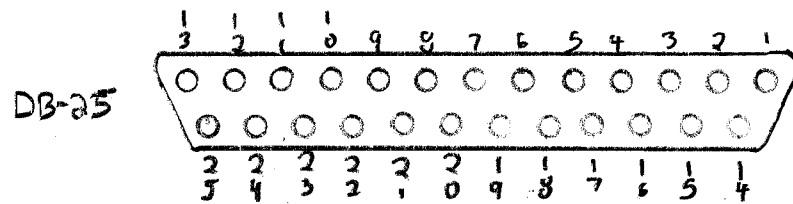
DIP HEADER MODULE



3M SERIAL KBD
INTERFACE
2400 BAUD BN1
10 of 10



Port = $(64 + 32 + 16) = 112_{10} = \text{ASCII DATA OUT}$
 $113_{10} = \text{STATUS IN}$
 $114_{10} = \text{CONTROL OUT}$
 $115_{10} = 130_{10} = \text{CONFIG}$



PARALLEL PRINTER CONFIG.
 NOTES A

DIR	NAME	DB	B255	Q255	CENT
→	STROBE	1	PC0	14	1
→	D0	2	PA0	4	2
→	D1	3	PA1	3	3
→	D2	4	PA2	2	4
→	D3	5	PA3	1	5
→	D4	6	PA4	40	6
→	D5	7	PA5	39	7
→	D6	8	PA6	38	8
→	D7	9	PA7	37	9
←	ACK	10	PB6	24	10
←	BUSY	11	PB7	25	11
←	PAP-OUT	12	PB5	23	12
←	SEL	13	PB4	22	13
→	LF	14	PC1	15	14
←	ERROR	15	PB3	21	32
→	INIT	16	PC2	16	31
→	SEL	17	PC3	17	36
		18	Vss		19-29
		19			16
		20			33
		21			30
		22			
		23			
		24			
		25			
		26	Vss		
	N/C				34
	+5Vdc				35

NEILB I/O

C COM 121₁₀
C DATA 120₁₀

LSTAT 123₁₀
L COM 123₁₀
L DATA 122₁₀

CKBR ~~00000000~~ 10B
CPTR ~~00000000~~ 1B

L2BIT ~~00000000~~ 1B

SET TARBELL MEMORY SIZE

	<u>64K</u>	<u>60K</u>	<u>56K</u>
ED CBIOSxx	64	60	56
ED CBOOTxx	64	60	56
ASM CBIOSxx	64	60	56
ASM SBOOTxx	64	60	56
MOV CPM (xx-1)*	63	59	55
SAVE 32 CPM(xx-1).COM	63	59	55
DDT CPM(xx-1).COM	63	59	55
I CBIOSxx HEX	64	60	56
Rnnnn	2480	3480	4480
ISBOOTxx.HEX	64	60	56
R900			
<CTRL>-C			
SYSGEN			
↓			
A or B			
↓			

CPM 1.4 CONFIG FOR NEILB I/O
NOTES B