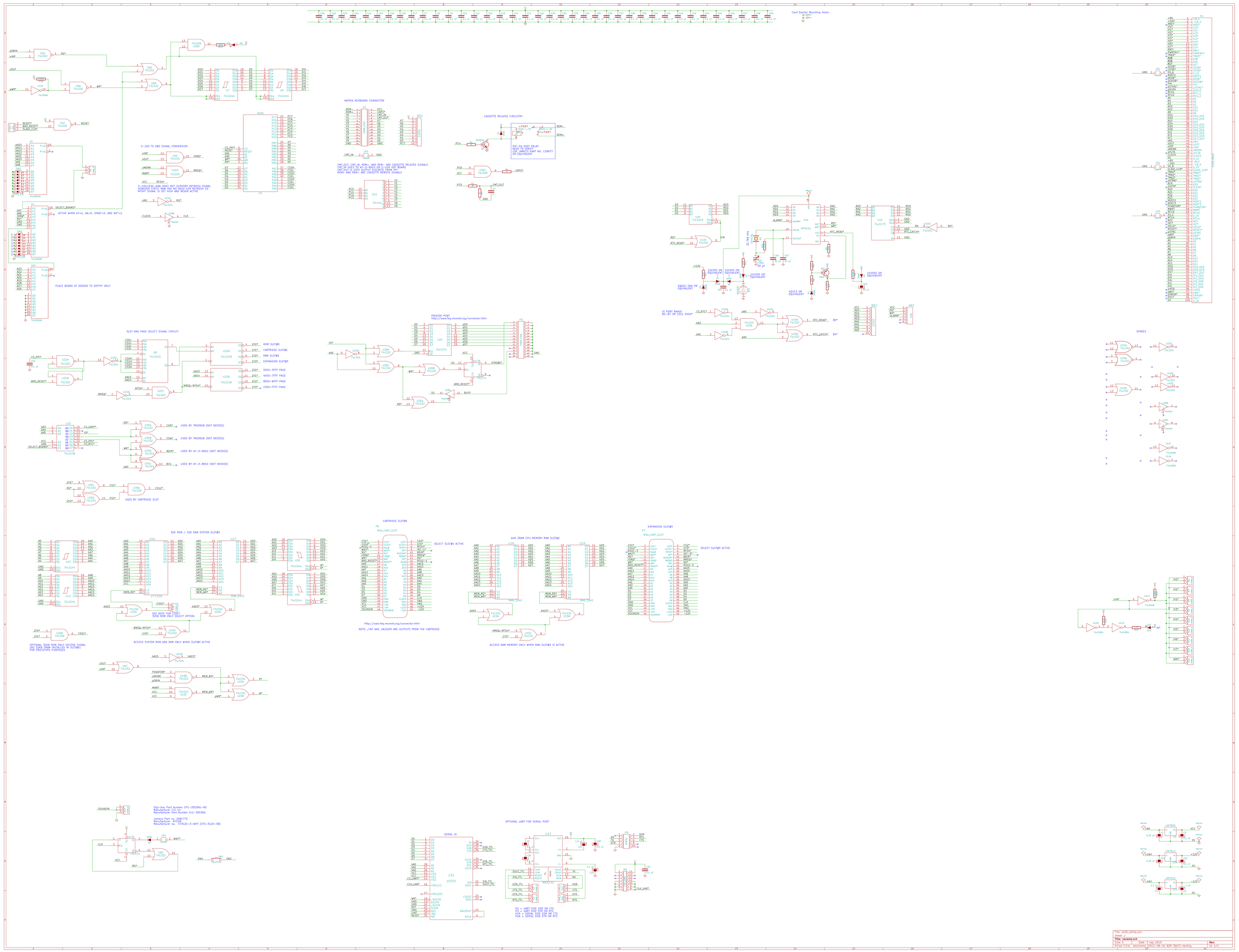
Card Ejector Mounting Holes
MATRIX KEYBOARD CONNECTOR
CASSETTE RELATED CIRCUITRY
DIP-16, DSDT RELAY
NEED TO VERIFY
USE JAMECO PART NO. 139977
OR EQUIVALENT
CMT_OUT, CMT_IN, REM+, AND REM- ARE CASSETTE RELATED SIGNALS
CMT_IN GOES TO AY-3-8910 ON S-100 VDP BOARD
CMT_OUT IS GPIO OUTPUT DISCRETE FROM PPI
REM+ AND REM- ARE CASSETTE REMOTE SIGNALS
S-100 TO Z80 SIGNAL CONVERSION
S-100/IEEE-696 DOES NOT SUPPORT REFRESH SIGNAL
HOWEVER STATIC RAM HAS NO NEED FOR REFRESH SO
RFSH* SIGNAL IS SET HIGH AND NEVER ACTIVE
ACTIVE WHEN A7=1, A6=0, IORQ*=0, AND M1*=1
PLACE BOARD AT 000000 TO 00FFFF ONLY
SLOT AND PAGE SELECT SIGNAL CIRCUIT
ROM SLOT#0
CARTRIDGE SLOT#1
RAM SLOT#2
EXPANSION SLOT#3
0000-3FFF PAGE
4000-7FFF PAGE
8000-BFFF PAGE
C000-FFFF PAGE
PRINTER PORT
http://www.faq.msxnet.org/connector.html
IO PORT RANGE
B0-B7 OR 1011 0XXX*
USED BY TMS9918 (NOT NEEDED)
USED BY AY-3-8910 (NOT NEEDED)
USED BY CARTRIDGE SLOT
SPARES
32K ROM / 32K RAM SYSTEM SLOT#0
CARTRIDGE SLOT#1
SELECT SLOT#1 ACTIVE
64K SRAM CPU MEMORY RAM SLOT#2
EXPANSION SLOT#3
SELECT SLOT#3 ACTIVE
OPTIONAL 32KB ROM ONLY DECODE SIGNAL
(NO 32KB SRAM INSTALLED IN SLOT#0)
FOR PROTOTYPE PURPOSES
SEE NOTE FOR CSO1*
32KB ROM ONLY SELECT OPTION
ACCESS SYSTEM ROM AND RAM ONLY WHEN SLOT#0 ACTIVE
ACCESS RAM MEMORY ONLY WHEN RAM SLOT#2 IS ACTIVE
Digi-Key Part Number CP1-3553NG-ND
Manufacturer CUI Inc
Manufacturer Part Number SJ1-3553NG
Jameco Part no. 2081772
Manufacturer KYCON
Manufacturer no. STX120-3-WHT (STX-3120-38)
OPTIONAL UART FOR SERIAL PORT
SERIAL IO
K2 = UART SIDE DSR OR CTS
K3 = UART SIDE DTR OR RTS
K15 = SERIAL SIDE DSR OR CTS
K16 = SERIAL SIDE DTR OR RTS
File: s100_Utility.sch
Sheet: /
Title: noname.sch
Size: E Date: 3 sep 2013 Rev:
KiCad (2.4. eeschema (2012-08-04 BZR 3667)-testing Id: 1/1