

256KZ Random Access Memory

Instruction

Manual

Cromemco® 256KZ

Instruction Manual

CROMEMCO, Inc. 280 Bernardo Avenue Mountain View, CA. 94043

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TABLE OF CONTENTS

Chapter 1: INTRODUCTION	1
68000 Based Systems Z-80 Based Systems Features	1 1 2
Chapter 2: USING THE 256KZ IN A Z-80 SYSTEM	3
Selecting a PROM Switch Settings	3 3
ရေးရ ကို မြောင်းများ မြောင်းများ မြောင်းများ မြောင်းများ မြောင်းများ မြောင်းများ မြောင်းများ မြောင်းများ မြောင	
Chapter 3: USING THE 256KZ IN A 68000 SYSTEM	7
Selecting a PROM Switch Settings	7 7
Appendix A: LIMITED WARRANTY	11

FEATURES

The 256KZ automatically switches from contiguous memory when used with a DPU based 68000 Cromix system to bank selected memory in Z-80 Cromix systems. The switch settings on the board determine its address range in 68000 memory space. The PROM plugged into the IC39 socket determines the memory banks selected in Z-80 systems. This means that with Cromemco's dual processor D-series systems you can use either the Z-80 or 68000 Cromix Operating System interchangeably, without changing any settings on the 256KZ memory boards.

The 256KZ can be byte-addressed or word-addressed. During Z-80 memory operations eight bits of data are transferred at one time, while with 68000 memory operations, 16 bits of data are transferred at one time. In this way, there is no speed degradation when using the 256KZ with 68000 systems.

Chapter 2

USING THE 256KZ IN A Z-80 SYSTEM

SELECTING A PROM

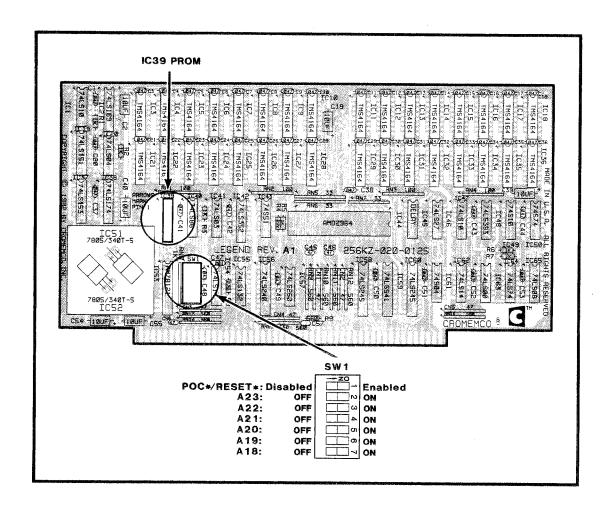
Determination of the 64 Kbyte banks of memory which a board contains is made via a PROM, IC39. The 256KZ is shipped with a Cromemco 74947 PROM in this location. This PROM places memory banks zero through three in the 256KZ as required by a 3-user Z-80 Cromix system.

If two 256KZ boards are being used for a 6-user Z-80 Cromix system, the 74947 PROMs must be removed from both boards and new PROMs installed. Cromemco PROM number 74948 is installed in one board, placing memory banks zero through three in the 256KZ as required by a 6-user system. (This is the board with switch bit one ON, as explained below.) Cromemco PROM number 74949 is installed in the other board, placing memory banks four through six in this 256KZ as required by a 6-user system. (This is the board with switch bit one OFF, as explained below.) Refer to the illustration and table at the end of this chapter.

If no PROM is in the IC39 socket, the 256KZ will assume that it is operating in a 68000 based system and that its addresses are being set by the switches (refer to Chapter 3).

SWITCH SETTINGS

There is only one switch bit which affects the operation of a 256KZ board in a Z-80 based system. This is switch bit one (labeled POC*/RESET* in the illustration) which enables the bottom 32 Kbytes of the board when the system is first turned on or reset.



Switch bits two through seven are only significant in 68000 based Cromix systems.

Switch bit one must ON in systems using one 256KZ board.

In Z-80 based systems incorporating two 256KZ boards, one board contains memory banks zero through three while the other contains banks four through six. Switch bit one must be in the ON position on the board containing banks zero through three and OFF on the other board. Refer to the following table.

Cromix System	256KZ #1	256KZ #2
3 user	switch bit l - ON PROM as shipped (74947)	not applicable
6 user	switch bit 1 - ON change PROM to 74948	switch bit 1 - OFF change PROM to 74949

Settings of other switches on the board are not relevant to 2--80 operation.

Cromemco 256KZ Memory Board

Chapter 3

USING THE 256KZ IN A 68000 SYSTEM

SELECTING A PROM

The IC39 PROM has no effect on the operation of a 256 KZ board in a 68000 based system. If the 256 KZ board is to be used with the Cromemco DPU board in both a Z-80 and 68000 environments, a PROM accommodating the needs of the Z-80 portion of the DPU should be used. Refer to Chapter 2 for a description of the 256 KZ in a Z-80 environment.

SWITCH SETTINGS

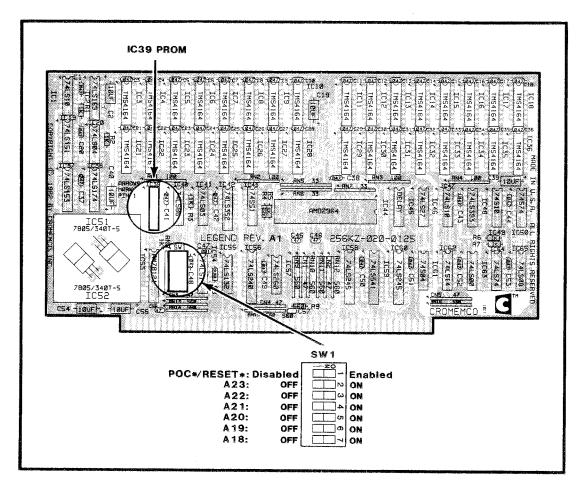
When switch bit one (labeled POC*/RESET* in the illustration) is on, it enables the bottom 32 Kbytes of the board when the system is first turned on or reset. Switch bit one must be in the ON position on the 256KZ board addressed as the lowest area of memory. It must be OFF on all other 256KZ boards in the system.

Switch bits two through seven are used to select the upper six address bits for 68000 operation. Refer to the following illustration.

The first (or only) 256KZ board in any 68000 based system should have switch bits two through seven ON. (This is the board with switch bit one ON, as explained above.) This causes the board to be addressed starting at location zero. It must also have the standard PROM (74947) in the IC39 socket. This enables memory to be accessed by the DPU during start-up procedures.

The second 256KZ board in the system should have switch bit seven OFF and bits two through six ON. (This and all subsequent boards should have switch bit one OFF, as explained above.) The second and subsequent boards in

the system do not require a PROM. If there is no PROM in the IC39 socket, the 256KZ will assume that it is in a 68000 based system.



For switch settings in systems with more than two 256KZ boards, consult the following table.

First Memory Address			Sw	itch	Bit		
Hexadecimal (0000)	Decimal (K)	2	3	4	5	6	7
00 04 08 0C 10 14	0 256 512 1024 1280 1536	ON ON ON ON	ON ON ON ON	ON ON ON ON ON	ON ON ON ON OFF	ON ON OFF OFF ON	ON OFF ON OFF ON OFF
F4 F8 FC	15616 15872 16128	OFF OFF OFF	OFF OFF OFF	OFF OFF OFF	OFF OFF OFF	ON OFF OFF	OFF ON OFF

Appendix A LIMITED WARRANTY

Cromemco, Inc. ("Cromemco") warrants this product against defects in material and workmanship to the original purchaser for ninety (90) days from the date of purchase, subject to the following terms and conditions.

What Is Covered By This Warranty:

During the ninety (90) day warranty period Cromemco will, at its option, repair or replace this Cromemco product or repair or replace with new or used parts any parts or components, manufactured by Cromemco, which prove to be defective, provided the product is returned to an Authorized Cromemco Dealer as set forth below.

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- 2. the return authorization number.
- 3. a description of the problem, and
- 4. proof of the date of retail purchase.

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Other Important Provisions:

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